ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention	LOW TRIGGER VOLTAGE ESD NMOSFET TRIPLE-WELL CMOS DEVICES

Submision Type : Utility Patent Filing

Application Number:

10/709041

EFS ID: 58673

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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IGEN77	Filename= N/A BusinessRule= The access code is not included which allows for automated processing to the deposit account. #Supporting Msg:Either <us-access-code> not found in fee-xml OR actual access code not provided in the fucntion call.

First Named Applicant: Kiran Chatty

Attorney Docket Number: BUR920030188US1

Timestamp: 2004-04-08 10:56:57 EDT

From: us

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us-ids	BUR920030188US1-usidst.xml	2231	2004-04-08
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us-assignment	BUR920030188Assign2.tif	14604	2004-04-08
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us-declaration	BUR920030188dec2.tif	16828	2004-04-08
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application-body	application-body.dtd	49498	2004-04-08
application-body	wipo.ent	4956	2004-04-08
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application-body	mathml2-qname-1.mod	13225	2004-04-08

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application-body	BUR920030188drw6.tif	6952	2004-04-08
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